

Distribution of students in experimental and simulation sessions

Afternoon : 14:00 to 17:30

The total of 60 students has been divided broadly into two batches : Batch I and Batch II.

1. Batch I will do simulation sessions from November 7 to November 18.
2. Batch I will do detector sessions from November 20 to November 27.
3. Batch II will do detector sessions from November 7 to November 14.
4. Batch II will do simulation sessions from November 15 to November 27.

Each batch has been divided into two 6 groups. The division of students in groups and batches are tabulated in Table 1 and Table 2. The distribution of experiments to each group is tabulated in Table 3 and Table 4.

Batch I

Table 1: Students in Batch I

Batch	Sl. No	Name	University/Institute
G1	1	Ms. Sandeep Kaur	PU, Chandigarh
	2	Ms. Anupam Yadav	University of Lucknow, Lucknow
	3	Mr. Debasish Mallick	NISER, Jatni
	4	Mr. Sumit Kumar Saha	VECC, Kolkata
	5	Mr. Muhammad Alibordi	IITM, Chennai
G2	1	Mr. Ashutosh Dash	NISER, Jatni
	2	Ms. Renu Garg	PU, Chandigarh
	3	Mr. Krishnakumar Ravindran	IITM, Chennai
	4	Mr. Kousik Naskar	IITB, Mumbai
	5	Mr. Vinayakrishnan MB	IOP, Bhubaneswar
G3	1	Ms. Anjali Sharma	PU, Chandigarh
	2	Ms. Mitali Mondal	VECC, Kolkata
	3	Mr. Asar Ahmed	University of Delhi, Delhi
	4	Mr. Anil Kumar	TIFR, Mumbai
	5	Mr. Shubham Pandey	IISER, Pune
G4	1	Ms. Sanchari Thakur	VECC, Kolkata
	2	Ms. Elizabeth George	IITB, Mumbai
	3	Mr. Sushanta Tripathy	IIT-Indore, Indore
	4	Mr. Arpit Singh	BHU, Varanasi
	5	Mr. Chandiprasad Kar	NISER, Jatni
G5	1	Mr. Debabrata Bhowmik	SINP, Kolkata
	2	Mr. Jaydeep Datta	HBNI, Mumbai
	3	Ms. Srishti Nagu	University of Lucknow, Lucknow
	4	Mr. Baidyanath Sahoo	IITB, Mumbai
	5	Ms. Sinjini Chandra	VECC, Kolkata
G6	1	Mr. Prasant Kumar Rout	SINP, Kolkata
	2	Mr. Dukhishyam Mallick	NISER, Jatni
	3	Ms. Meena	PU, Chandigarh
	4	Ms. Meenakshi Sharma	University of Jammu, Jammu
	5	Mr. Somen Gope	Gauhati University, Guwahati

Batch II

Table 2: Students in Batch II

Batch	Sl. No	Name	University/Institute
G7	1	Mr. Debashis Sahoo	TIFR, Mumbai
	2	Ms. Saswati Nandan	SINP, Kolkata
	3	Mr. Jagbir Singh	PU, Chandigarh
	4	Ms. Dipika Dash	NISER, Jatni
	5	Mr. Abhishek Kumar	BHU, Varanasi
G8	1	Mr. Sridhar Tripathy	SINP, Kolkata
	2	Mr. Tanay Dey	TIFR, Mumbai
	3	Ms. Sagarika Swain	IOP, Bhubaneswar
	4	Mr. Sourav Kundu	NISER, Jatni
	5	Ms. Niharika Rout	IITM, Chennai
G9	1	Mr. Sumit Kumar	PU, Chandigarh
	2	Mr. Gourab Saha	SINP, Kolkata
	3	Mr. Praveen Chandra Tiwari	IISc, Bengaluru
	4	Ms. Shreya Roy	Bose Institute, Kolkata
	5	Mr. Sushant Kumar Singh	VECC, Kolkata
G10	1	Mr. Vijay J. Iyer	NISER, Jatni
	2	Mr. Pranjal Sarma	Gauhati University, Guwahati
	3	Mr. Om Shiv Krishna Chaturvedi	BHU, Varanasi
	4	Ms. Shreyasi Acharya	VECC, Kolkata
G11	1	Mr. Sandeep Dudi	PU, Chandigarh
	2	Mr. Sudip Mandal	NISER, Jatni
	3	Mr. Sayan Ghosh	SINP, Kolkata
	4	Ms. Sabeeha Naaz	University of Lucknow, Lucknow
	5	Mr. Chandrasekhar Ghosh	VECC, Kolkata
G12	1	Mr. Arnab Purohit	SINP, Kolkata
	2	Mr. P. Kalyana Chakravarty	VECC, Kolkata
	3	Mr. Souvik Maity	IITBBS, Bhubaneswar
	4	Mr. Randhir Singh	University of Jammu, Jammu
	5	Ms. Lata Panwar	IISc, Bengaluru

Table 3: Distribution of Experiments for Batch I

Batch/Days	Day 1	Day 2	Day 3	Day 4	Day 5	Day 6	Day 7(Pres)
	November 7	November 8	November 9	November 10	November 11	November 13	November 14
G1	SC	RPC	FPGA	GEM	PC	LV	G1 (SC)
G2	LV	SC	RPC	FPGA	GEM	PC	G2 (LV)
G3	PC	LV	SC	RPC	FPGA	GEM	G3 (PC)
G4	GEM	PC	LV	SC	RPC	FPGA	G4 (GEM)
G5	FPGA	GEM	PC	LV	SC	RPC	G5 (FPGA)
G6	RPC	FPGA	GEM	PC	LV	SC	G6 (RPC)
G(1-6)	ML						

Table 4: Distribution of Experiments for Batch II

Batch/Days	Day 1	Day 2	Day 3	Day 4	Day 5	Day 6	Day 7(Pres)
	November 20	November 21	November 22	November 23	November 24	November 25	November 27
G7	SC	RPC	FPGA	GEM	PC	LV	G7 (SC)
G8	LV	SC	RPC	FPGA	GEM	PC	G8 (LV)
G9	PC	LV	SC	RPC	FPGA	GEM	G9 (PC)
G10	GEM	PC	LV	SC	RPC	FPGA	G10 (GEM)
G11	FPGA	GEM	PC	LV	SC	RPC	G11 (FPGA)
G12	RPC	FPGA	GEM	PC	LV	SC	G12 (RPC)
G(7-12)	ML						

- SC – Scintillator
- RPC – Resistive Plate chamber : RPC1 (Fabrication) or RPC2 (Testing)
- PC – Proportional counter
- LV – Labview
- GEM – Gas Electron Multiplier
- FPGA – Field Programmable Gate Array
- ML – Muon Lifetime (Common experiment for all batches)